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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *TH*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *TH*  
 JAPAN 2001-128368 04/25/2001

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>[Signature]</i> Examiners Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 34	TOTAL CLAIMS 12	INDEPENDENT CLAIMS 2
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 21171  
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TITLE  
 Instruction processing method for verifying basic instruction arrangement in VLIW instruction for variable length  
 VLIW processor

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